Report on the
SAT 2007
Conference on
Theory and
Applications of
Satisfiability Testing

João Marques-Silva,
Karem Sakallah, and Inês Lynce

Propositional satisfiability (SAT) was the first decision problem to be shown nondeterministic polynomial (NP) complete and has been the subject of remarkable theoretical work over the years. Moreover, SAT and extensions of SAT find many practical applications, including planning, software and hardware model checking, bioinformatics, equivalence checking, test-pattern generation, software package installation, and cryptography.

The annual SAT conference is now widely recognized as “the venue” for publishing the latest advances in SAT research. This year marked the tenth SAT meeting. The conference was held in Lisbon, Portugal, from May 28 through May 31, 2007.

The area of SAT is interpreted in a broad sense to include not just propositional satisfiability, but also pseudo-Boolean (PB) constraint solving and optimization, quantified Boolean formula (QBF), constraint programming (CP) techniques for word-level problems and their propositional encoding, and satisfiability modulo theories (SMT). Submissions were solicited for original research on proof systems and proof complexity, search algorithms and heuristics, analysis of algorithms, hard instances, randomized formulae, problem encodings, industrial applications, solvers, simplifiers and tools, case studies and empirical results.

The conference attracted 80 participants, from academe and industry—attendance that made the 2007 meeting one of the largest SAT conferences in terms of attendance. The conference included a comprehensive social program to stimulate communication and exchange of ideas among conference participants.

A total of 34 papers were presented at the conference, representing 22 regular papers (14 pages, 25-minute presentation) and 12 short papers (6 pages, 12-minute presentation). The accepted papers were organized into nine sessions, and their full text was included in the proceedings. The conference program also featured two invited presentations. The first, by Martin Davis, professor emeritus at New York University, USA, chronicled the original development of the Davis-Putnam-Loveland-Logemann (DPLL) algorithm and proposed an unorthodox take on the $P \neq NP$ problem. The second, by Andrei Voronkov, professor at the University of Manchester, UK, addressed new encodings that enable succinct representations of certain combinatorial problems in the Bernays-Schonfinkel fragment of first-order logic.

A number of additional events were associated with the SAT conference, including the well-known SAT competition, the QBF evaluation, the PB evaluation, and the MAX-SAT evaluation.

A poster session took place at the last day of the conference, permitting the results from solver competitions and evaluations, and also allowing some papers to provide additional detail of their contribution. To further stimulate the development of solver technology in the area of SAT, the SAT 2007 conference organized a special session on SAT solving. Armin Biere, professor at the Johannes Kepler University, Linz, Austria, was the invited speaker for this special session, having addressed design and implementation issues in modern SAT solvers. The special session also included a panel discussion on the development of modern SAT solvers. The panelists included the authors of some of the best performing SAT solvers in different categories, Armin Biere, Marijn Heule, and Knot Pipatsrisawat.

The conference website contains additional information regarding the conference, including the slides of all talks.

The next International Conference on Theory and Applications of Satisfiability Testing will be held in Guangzhou, China, 12–15 May 2008.
Marques-Silva can be reached via e-mail at jpms@ecs.soton.ac.uk.

Karem A. Sakallah is a professor of electrical engineering and computer science at the University of Michigan, Ann Arbor. Prior to joining Michigan he was on the technical staff of Digital Equipment Corporation, Hudson, Massachusetts. His research portfolio includes the development of novel functional and timing analysis and simulation algorithms for integrated circuit chips, as well as the development of scalable hardware and software verification methods based on powerful Boolean reasoning techniques. Sakallah received his Ph.D. degree in electrical and computer engineering from Carnegie Mellon University and is a Fellow of the Institute of Electrical and Electronics Engineers.

Inês Lynce has been, since 2005, an assistant professor of computer engineering in the Instituto Superior Técnico at the Technical University of Lisbon. She is also a senior researcher at INESC-ID. She has been a visitor to Cornell University (USA), Université d’Artois (France), University of St. Andrews (Scotland), University College Cork (Ireland), and University of Southampton (England). Her research interests are centered in modeling and solving combinatorial problems using efficient techniques, including symmetry breaking, constraint programming, and Boolean satisfiability solvers. She can be reached via e-mail at ines@sat.inesc-id.pt.

Note
1. See sat07.ecs.soton.ac.uk

João Marques-Silva is a professor of computer science in the School of Electronics and Computer Science at the University of Southampton, UK. He received a Ph.D. degree in electrical engineering and computer science in 1995 from the University of Michigan, Ann Arbor, USA, and the habilitation degree in computer science from the Technical University of Lisbon, Portugal, in 2004. His research interests include formal methods, namely system specification, verification and model checking, algorithms for Boolean satisfiability and extensions, and application of formal methods in artificial intelligence, computational biology, and design automation.

Marques-Silva can be reached via e-mail at jpms@ecs.soton.ac.uk.

Karem A. Sakallah is a professor of electrical engineering and computer science at the University of Michigan, Ann Arbor. Prior to joining Michigan he was on the technical staff of Digital Equipment Corporation, Hudson, Massachusetts. His research portfolio includes the development of novel functional and timing analysis and simulation algorithms for integrated circuit chips, as well as the development of scalable hardware and software verification methods based on powerful Boolean reasoning techniques. Sakallah received his Ph.D. degree in electrical and computer engineering from Carnegie Mellon University and is a Fellow of the Institute of Electrical and Electronics Engineers.

Inês Lynce has been, since 2005, an assistant professor of computer engineering in the Instituto Superior Técnico at the Technical University of Lisbon. She is also a senior researcher at INESC-ID. She has been a visitor to Cornell University (USA), Université d’Artois (France), University of St. Andrews (Scotland), University College Cork (Ireland), and University of Southampton (England). Her research interests are centered in modeling and solving combinatorial problems using efficient techniques, including symmetry breaking, constraint programming, and Boolean satisfiability solvers. She can be reached via e-mail at ines@sat.inesc-id.pt.

Figure 1. The SAT Conference Was Held in Lisbon, Portugal.